

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Chang
Application Number: 10/823,513
Filing Date: April 12, 2004
Title: Autorun for Integrated Circuit Memory Component

Examiner: Rampuria, Satish
Art Unit: 2191

SUPPLEMENTAL AMENDMENT REMARKS

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Applicant submit the following supplemental remarks in addition to those submitted in the April 30, 2008 amendment responding to the Patent and Trademark Office action dated October 31, 2007: